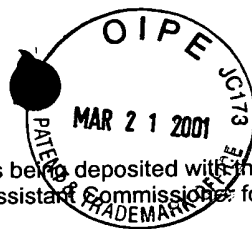


Docket No.: GR 98 P 1916 P



RECEIPT

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By: Loren D. Pearson

Date: March 16, 2001

6 / CFR
E. Millis
7-19-01

IN THE UNITED STATES PATENT AND TRADEMARK OFFICE

Applicant : Wilhelm Schmid, et al.
Appl. No. : 09/740,634
Filed : 12/18/00
Title : Device for Measurement and Analysis of Electrical Signals of an Integrated Circuit Component
Art Unit : 2858

LETTER

Hon. Commissioner of Patents and Trademarks,
Washington, D.C. 20231

Sir:

Undersigned counsel has received the Filing Receipt for the above-identified application.

However, the priority information has been listed incorrectly and should be listed as:

FED REP GERMANY 198 26 825.4 06/16/1998

The continuing information has been listed incorrectly and should be listed as:

THIS APPLN IS A CON OF PCT/DE99/01719 06/11/1999

It is respectfully requested that the Patent Office Records be changed and that a new Filing Receipt be issued, so that the printed patent will show the correct priority and continuing information.

Respectfully submitted,

Loren D. Pearson

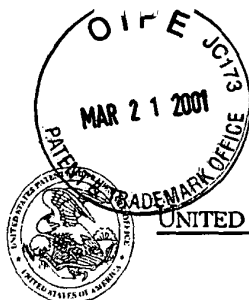
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Date: March 16, 2001

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UNITED STATES PATENT AND TRADEMARK OFFICE

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WASHINGTON, D.C. 20231
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APPLICATION NUMBER	FILING DATE	GRP ART UNIT	FIL FEE REC'D	ATTY. DOCKET NO	DRAWINGS	TOT CLAIMS	IND CLAIMS
09/740,634	12/18/2000	2858	710	GR 98 P 1916	4	16	2

CONFIRMATION NO. 4357

FILING RECEIPT



OC000000005848985

LERNER AND GREENBERG, P.A.
PATENT ATTORNEYS AND ATTORNEYS AT LAW
Post Office Box 2480
Hollywood, FL 33022-2480

Date Mailed: 03/12/2001

Receipt is acknowledged of this nonprovisional Patent Application. It will be considered in its order and you will be notified as to the results of the examination. Be sure to provide the U.S. APPLICATION NUMBER, FILING DATE, NAME OF APPLICANT, and TITLE OF INVENTION when inquiring about this application. Fees transmitted by check or draft are subject to collection. Please verify the accuracy of the data presented on this receipt. If an error is noted on this Filing Receipt, please write to the Office of Initial Patent Examination's Customer Service Center. Please provide a copy of this Filing Receipt with the changes noted thereon. If you received a "Notice to File Missing Parts" for this application, please submit any corrections to this Filing Receipt with your reply to the Notice. When the PTO processes the reply to the Notice, the PTO will generate another Filing Receipt incorporating the requested corrections (if appropriate).

Applicant(s)

Wilhelm Schmid, Ingolstadt, GERMANY;
Carsten Dorrhofer, Holzkirchen, GERMANY;

Continuing Data as Claimed by Applicant

For information should be here
Foreign Applications

GERMANY 198 26 825.4 06/16/1998
PCT/DE99/01719 06/11/1999

If Required, Foreign Filing License Granted 03/09/2001

Projected Publication Date: To Be Determined - pending completion of Missing Parts

Non-Publication Request: No

Early Publication Request: No

Title

Device for measurement and analysis of electrical signals of an integrated circuit component

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Preliminary Class
324

Data entry by : SANDARA, HOA

Team : OIPE

Date: 03/12/2001

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Title 35, United States Code, Section 184
Title 37, Code of Federal Regulations, 5.11 & 5.15

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NOT GRANTED

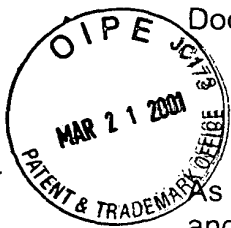
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PLEASE NOTE the following information about the Filing Receipt:

- The articles such as "a," "an" and "the" are not included as the first words in the title of an application. They are considered to be unnecessary to the understanding of the title.
- The words "new," "improved," "improvements in" or "relating to" are not included as first words in the title of an application because a patent application, by nature, is a new idea or improvement.
- The title may be truncated if it consists of more than 600 characters (letters and spaces combined).
- The docket number allows a maximum of 25 characters.
- If your application was submitted under 37 CFR 1.10, your filing date should be the "date in" found on the Express Mail label. If there is a discrepancy, you should submit a request for a corrected Filing Receipt along with a copy of the Express Mail label showing the "date in."
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Any corrections that may need to be done to your Filing Receipt should be directed to:

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Washington, DC 20231



Docket No.: GR 98 P 1916

COMBINED DECLARATION AND POWER OF ATTORNEY
IN ORIGINAL APPLICATION

As a below named inventor, I hereby declare that: my residence, post office address and citizenship are as stated below next to my name; that I verily believe that I am the original, first and sole inventor (if only one name is listed below) or an original, first and joint inventor (if plural names are listed below) of the subject matter which is claimed and for which a patent is sought on the invention entitled:

DEVICE FOR MEASUREMENT AND ANALYSIS OF ELECTRICAL SIGNALS OF
AN INTEGRATED CIRCUIT COMPONENT

described and claimed in the specification bearing that title, that I understand the content of the specification, that I do not know and do not believe the same was ever known or used in the United States of America before my or our invention thereof, or patented or described in any printed publication in any country before my or our invention thereof or more than one year prior to this application, that the same was not in public use or on sale in the United States of America more than one year prior to this application, that the invention has not been patented or made the subject of an inventor's certificate issued before the date of this application in any country foreign to the United States of America on an application filed by me or my legal representatives or assigns more than twelve month prior to this application, that I acknowledge my duty to disclose information of which I am aware which is material to the examination of this application under 37 C.F.R. 1.56a, and that no application for patent or inventor's certificate of this invention has been filed earlier than the following in any country foreign to the United States prior to this application by me or my legal representatives or assigns:

German Application No. 198 26 825.4, filed June 16, 1998, the International Priority of which is claimed under 35 U.S.C. §119; and International Application No. PCT/DE99/01719, filed June 11, 1999, the Priority of which is claimed under 35 U.S.C. §120.

I hereby appoint the following attorney(s) and/or agent(s) to prosecute this application and to transact all business in the Patent and Trademark Office connected therewith:

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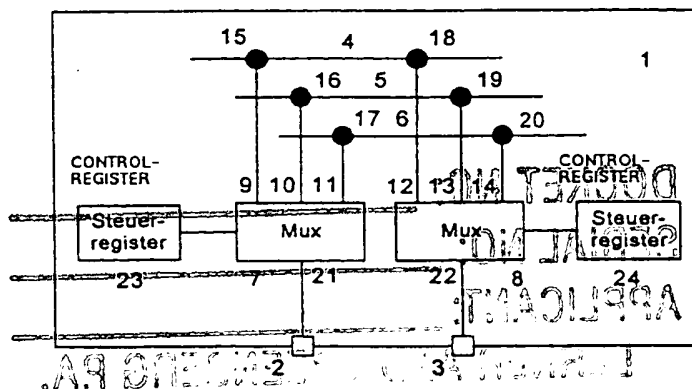


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INTERNATIONALE ANMELDUNG VERÖFFENTLICHT NACH DEM VERTRAG ÜBER DIE
INTERNATIONALE ZUSAMMENARBEIT AUF DEM GEBIET DES PATENTWESENS (PCT)

<p>(51) Internationale Patentklassifikation ⁶ : G01R 31/317</p>	A2	<p>(11) Internationale Veröffentlichungsnummer: WO 99/66337</p> <p>(43) Internationales Veröffentlichungsdatum: 23. Dezember 1999 (23.12.99)</p>
<p>(21) Internationales Aktenzeichen: PCT/DE99/01719</p> <p>(22) Internationales Anmeldedatum: 11. Juni 1999 (11.06.99)</p> <p>(30) Prioritätsdaten: 198 26 825.4 16. Juni 1998 (16.06.98) DE</p> <p>(71) Anmelder (für alle Bestimmungsstaaten ausser US): SIEMENS AKTIENGESELLSCHAFT [DE/DE]; Wittelsbacherplatz 2, D-80333 München (DE).</p> <p>(72) Erfinder; und (75) Erfinder/Anmelder (nur für US): SCHMID, Wilhelm [DE/DE]; Am Mühlbach 6, D-85055 Ingolstadt (DE). DÖRRHÖFER, Carsten [DE/DE]; Wilhelm-Leibl-Strasse 21, D-83607 Holzkirchen (DE).</p> <p>(74) Gemeinsamer Vertreter: SIEMENS AKTIENGESELLSCHAFT; Postfach 22 16 34, D-80506 München (DE).</p>		<p>(81) Bestimmungsstaaten: CN, JP, KR, US, europäisches Patent (AT, BE, CH, CY, DE, DK, ES, FI, FR, GB, GR, IE, IT, LU, MC, NL, PT, SE).</p> <p>Veröffentlicht <i>Ohne internationalen Recherchenbericht und erneut zu veröffentlichen nach Erhalt des Berichts.</i></p>

(54) Title: DEVICE FOR MEASURING AND ANALYZING ELECTRICAL SIGNALS OF AN INTEGRATED CIRCUIT COMPONENT

(54) Bezeichnung: EINRICHTUNG ZUR VERMESSUNG UND ANALYSE VON ELEKTRISCHEN SIGNALEN EINES INTEGRIERTEN SCHALTUNGSBAUSTEINS



(57) Abstract

According to the invention, one or more external test-connection contact points (pads; pins; balls), (2, 3) are provided in an integrated circuit component (chip) (1), through which signals (4, 5; 6) that are to be measured or analyzed are selectively fed, e.g. by means of a multiplex circuit (7, 8), and wherein the signals may be connected by means of routes located internally in the component from switch points that are not directly accessible, e.g. points inside the chip (15 to 20) or covered contact points. The device according to the invention is particularly useful for highly integrated semiconductor chips.



Device for measurement and analysis of electrical signals of
an integrated circuit component

Cross-Reference to Related Application:

This is a continuation of copending international application PCT/DE99/01719, filed June 11, 1999, which designated the United States.

Background of the Invention:

Field of the Invention:

The invention relates to a device for electrical measurement and analysis of electrical signals which are present at circuit points that are not directly accessible in an integrated circuit component (IC; integrated circuit; chip) which is provided with a large number of connecting contact points (pads; pins; balls).

In order to allow malfunctions and clock concepts to be analyzed on integrated circuit components, it is necessary to monitor internal-chip signals, that is to say signals which are present at inaccessible circuit points within the integrated circuit component. In this case, accurate timing of the signal monitoring is very frequently important. For example, to do this, clocks must be measured with respect to

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